

Thanks



**Thank you CoWare Inc. and Doulos Ltd.
for sponsoring our
9. European SystemC Users Group Meeting!**

Agenda 1900 - 1930

- **19:00 h: Welcome & Opening:
9. European SystemC Users Group Meeting**

Wolfgang Rosenstiel, University of Tübingen, ESCUG

Open SystemC Initiative:

- **SystemC News and OSCI Organizational Update**

Michael Meredith, OSCI Executive Officer, OSCI Board, USA

- **SystemC 2.1 Status and Features**

Stuart Swan, OSCI Language Working Group, USA

- **Technical Working Group Updates**

Stuart Swan, OSCI Language Working Group, USA

Michael Meredith, OSCI Executive Officer, OSCI Board, USA

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SystemC User Presentations:

- **19:30 h: A Functional Coverage Prototype for SystemC-based Verification of Chipset Designs**
Robert Siegmund, AMD Saxony LLC, Germany
Ulrich Hensel, AMD Saxony LLC, Germany
Andreas Herrholz, AMD Saxony LLC, Germany
Isa Volt, AMD Saxony LLC, Germany
- **19:45 h: System Architecture Performance Modeling with SystemC**
Soeren Sonntag, Infineon Technologies AG, Germany
Raimar Thudt, Infineon Technologies AG, Germany
Tino Bertram, Infineon Technologies AG, Germany
- **20:00 h: Abstract System-on-Chip Modelling in SystemC**
Kashif Virk, Technical University of Denmark, Denmark
Shanakar Mahadevan, Technical University of Denmark, Denmark
Jan Madsen, Technical University of Denmark, Denmark

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SystemC Suppliers Forum:

- **20:15 h: Generating TLM Bus Models from Formal Protocol Specifications**
Tom Michiels, CoWare, Inc., Belgium
- **20:30 h: Platform Creator/ConvergenSC & Incisive**
Mark Milligan, CoWare, Inc. USA
Frank Schirrmeister, Cadence, Germany
- **20:45 h: Embracing System Level Design**
Yossi Veller, Summit Design, Israel
- **21:00 h: End**