



# **SystemC Business Update**

**Kevin Kranen, Synopsys  
President, Board Member OSCI**

# OSCI Development Highlights

- Key Development Underway
  - OSCI Funding Key Projects
    - ◆ LRM and User Guide for v2.0 - Paul Menchini (did VHDL LRM) => 2H02
    - ◆ New SourceForge-base website => Online today
  - v2.0.1 out in April with SystemC regression suite
  - Five Working Groups Underway
    - ◆ IP Modeling, Verification, Dataflow, Language, Reference Implementation
    - ◆ Plus one “study group” - Mixed Signal
- Driving for IEEE Standardization within 2 years
- Engaging with other organizations to help lead in system space
  - VSIA
  - Accellera

# SystemC Roadmap



- New SourceForge-based website

March  
2002



- v2.0.1 bug fix release and regression tests

April  
2002



Working  
Group  
Specs

June  
2002



SystemC  
v2.0 LRM



- Working Group Code

Fall 2002

## Over the Last 3 Months:

- **New SystemC Products and Support from:**
  - Mentor Graphics - Seamless<sup>®</sup> C-Bridge(tm)
  - Verisity - SpecMan(tm) Elite
  - Forte Design Systems - ESC Library
  - Emulation & Verification Engineering - Zebu
- **New SystemC Capabilities from:**
  - Synopsys - CoCentric System Studio
  - CoWare N2C updated for SystemC 2.0
- **Plus Kluwer book - "System Design Using SystemC" underway for Spring 2002**

# Market Adoption

## ■ End Users

- Over 15,000 Licensees at over 500 companies/institutions
  - ◆ 3,000 regular downloaders with every release
- > 30K successful downloads of SystemC source code
- Unsolicited SystemC successes stories in conference proceedings
  - ◆ 10 Commercial ones - AMD, Infineon, Siemens, CSELT, FhG, STM
  - ◆ >10 more known ones slated for this year

## ■ EDA, IP and Services

- Over 25 Companies / over 40 Products in the works
  - ◆ EDA tools - 30
  - ◆ Training - 7
  - ◆ IP - 3
  - ◆ Open Source Resources - 10
- See <http://www.systemc.org> Products and Solution section for more details

# Strong SystemC Adoption since v1.0

