



4th European

SystemC Users Group Meeting

<http://www-ti.informatik.uni-tuebingen.de/systemc>

Copenhagen

October 5th, 2001, 1100-1600

The logo features the word "SYSTEMC" in white, uppercase letters, centered within a dark blue, semi-circular shape. A white arc is positioned above the text. The logo is set against a background of horizontal lines that transition from yellow at the top to blue at the bottom.

SYSTEMC™

SystemC Roadmap

SystemC v2.0 Innovation

- SystemC v1.0
 - RTL & behavioral level modeling (HDL & beyond)
 - integrated with higher level C/C++ functional modeling
- SystemC v2.0
 - provides higher levels of abstraction
 - enables modeling of HW / SW interaction
 - flexible communication channel refinement

SystemC Release Roadmap

- 1.0 - Hardware Design Flow
 - RTL and Behavioral Hardware Modeling
- 1.x - Master-Slave Communication Library
 - RPC-based untimed & timed functional modeling down to RTL for bus protocol based systems
- 2.0 - System Design Flow
 - General purpose communication and synchronization
 - Communication Refinement
 - Multiple, customizable models of computation
 - Dynamic thread creation

SystemC Release Roadmap (cont)

- 2.X - Extensions to System Design Flow
 - Fork & Join
 - Interrupt / abort for behavioral hierarchy
 - Performance modeling support
 - Timing specification and constraints

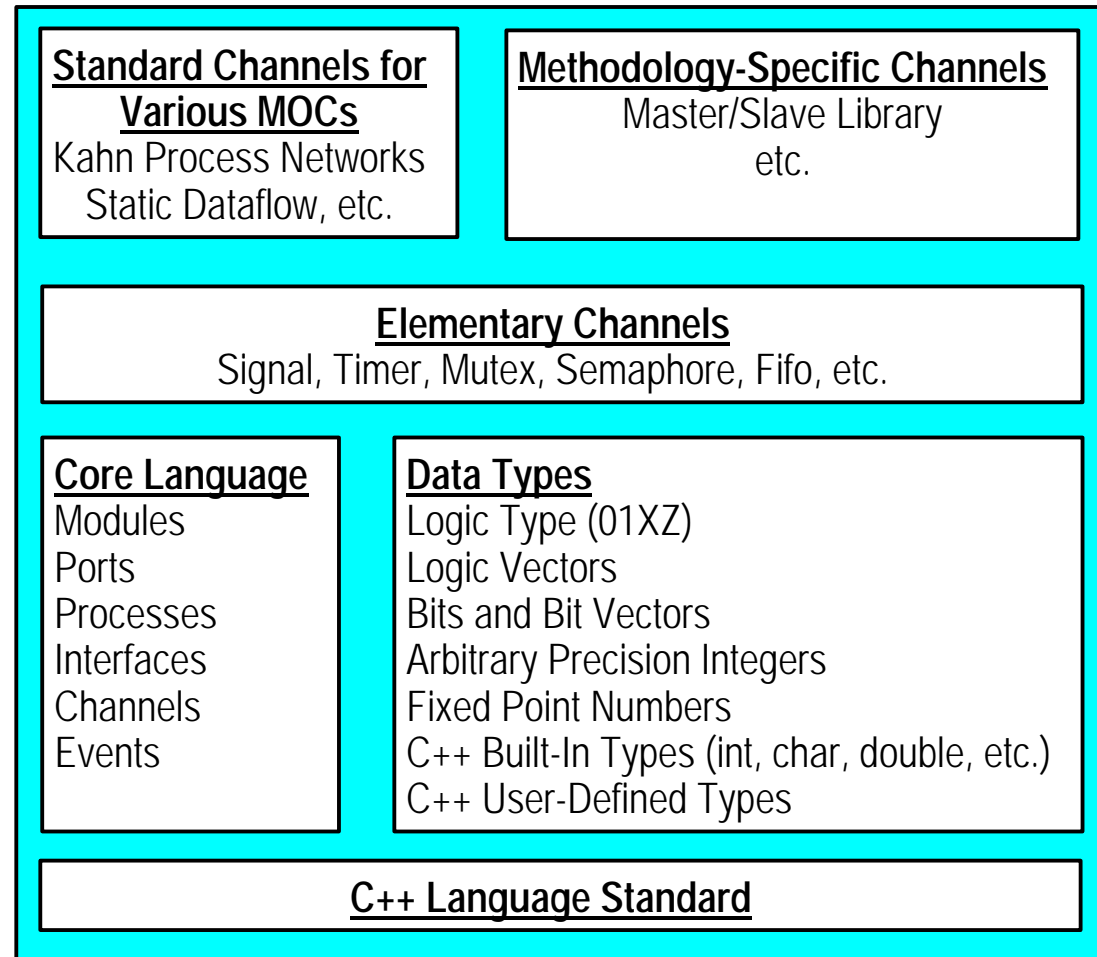
- 3.X Software Design Flow
 - Abstract RTOS modeling
 - Scheduler modeling

- 4.X - Analog / Mixed Signal Systems Modeling

SystemC 2.0 Language Architecture

*Upper layers
are built cleanly
on lower layers.*

*Lower layers
can be used
without upper
layers.*



V2.0 Reference Implementation Delivery

